

Z-80 instruction table

opcode	instruction	arguments	Z80	Z180	eZ80
5B	.LIL				1(2)
49	.LIS				1(2)
52	.SIL				1(2)
40	.SIS				1(2)
8E	ADC	A,(HL)	7	6	2
DD8EXX	ADC	A,(IX+dis)	19	14	4
FD8EXX	ADC	A,(IY+dis)	19	14	4
8F	ADC	A,A	4	4	1
88	ADC	A,B	4	4	1
89	ADC	A,C	4	4	1
8A	ADC	A,D	4	4	1
8B	ADC	A,E	4	4	1
8C	ADC	A,H	4	4	1
DD8C	ADC	A,IXH			2
DD8D	ADC	A,IXL			2
FD8C	ADC	A,IYH			2
FD8D	ADC	A,IYL			2
8D	ADC	A,L	4	4	1
CEXX	ADC	A,NN	7	6	2
ED4A	ADC	HL,BC	15	10	2
ED5A	ADC	HL,DE	15	10	2
ED6A	ADC	HL,HL	15	10	2
ED7A	ADC	HL,SP	15	10	2
86	ADD	A,(HL)	7	6	2
DD86XX	ADD	A,(IX+dis)	19	14	4
FD86XX	ADD	A,(IY+dis)	19	14	4
87	ADD	A,A	4	4	1
80	ADD	A,B	4	4	1
81	ADD	A,C	4	4	1
82	ADD	A,D	4	4	1
83	ADD	A,E	4	4	1
84	ADD	A,H	4	4	1
DD84	ADD	A,IXH			2
DD85	ADD	A,IXL			2
FD84	ADD	A,IYH			2
FD85	ADD	A,IYL			2
85	ADD	A,L	4	4	1
C6XX	ADD	A,NN	7	6	2
09	ADD	HL,BC	11	7	1
19	ADD	HL,DE	11	7	1
29	ADD	HL,HL	11	7	1
39	ADD	HL,SP	11	7	1
DD09	ADD	IX,BC	15	10	2
DD19	ADD	IX,DE	15	10	2
DD29	ADD	IX,IX	15	10	2
DD39	ADD	IX,SP	15	10	2
FD09	ADD	IY,BC	15	10	2
FD19	ADD	IY,DE	15	10	2
FD29	ADD	IY,IY	15	10	2
FD39	ADD	IY,SP	15	10	2
A6	AND	(HL)	7	6	2
DDA6XX	AND	(IX+dis)	19	14	4
FDA6XX	AND	(IY+dis)	19	14	4
A7	AND	A	4	4	1
DDA4	AND	A,IXH			2
DDA5	AND	A,IXL			2
FDA4	AND	A,IYH			2
FDA5	AND	A,IYL			2
A0	AND	B	4	4	1
A1	AND	C	4	4	1
A2	AND	D	4	4	1
A3	AND	E	4	4	1
A4	AND	H	4	4	1
A5	AND	L	4	4	1
E6XX	AND	NN	7	6	2
CB46	BIT	0,(HL)	12	9	3

opcode	instruction	arguments	Z80	Z180	eZ80
DDCBXX46	BIT	0,(IX+dis)	20	15	5
FDCBXX46	BIT	0,(IY+dis)	20	15	5
CB47	BIT	0,A	8	6	2
CB40	BIT	0,B	8	6	2
CB41	BIT	0,C	8	6	2
CB42	BIT	0,D	8	6	2
CB43	BIT	0,E	8	6	2
CB44	BIT	0,H	8	6	2
CB45	BIT	0,L	8	6	2
CB4E	BIT	1,(HL)	12	9	3
DDCBXX4E	BIT	1,(IX+dis)	20	15	5
FDCBXX4E	BIT	1,(IY+dis)	20	15	5
CB4F	BIT	1,A	8	6	2
CB48	BIT	1,B	8	6	2
CB49	BIT	1,C	8	6	2
CB4A	BIT	1,D	8	6	2
CB4B	BIT	1,E	8	6	2
CB4C	BIT	1,H	8	6	2
CB4D	BIT	1,L	8	6	2
CB56	BIT	2,(HL)	12	9	3
DDCBXX56	BIT	2,(IX+dis)	20	15	5
FDCBXX56	BIT	2,(IY+dis)	20	15	5
CB57	BIT	2,A	8	6	2
CB50	BIT	2,B	8	6	2
CB51	BIT	2,C	8	6	2
CB52	BIT	2,D	8	6	2
CB53	BIT	2,E	8	6	2
CB54	BIT	2,H	8	6	2
CB55	BIT	2,L	8	6	2
CB5E	BIT	3,(HL)	12	9	3
DDCBXX5E	BIT	3,(IX+dis)	20	15	5
FDCBXX5E	BIT	3,(IY+dis)	20	15	5
CB5F	BIT	3,A	8	6	2
CB58	BIT	3,B	8	6	2
CB59	BIT	3,C	8	6	2
CB5A	BIT	3,D	8	6	2
CB5B	BIT	3,E	8	6	2
CB5C	BIT	3,H	8	6	2
CB5D	BIT	3,L	8	6	2
CB66	BIT	4,(HL)	12	9	3
DDCBXX67	BIT	4,(IX+dis)	20	15	5
FDCBXX67	BIT	4,(IY+dis)	20	15	5
CB67	BIT	4,A	8	6	2
CB60	BIT	4,B	8	6	2
CB61	BIT	4,C	8	6	2
CB62	BIT	4,D	8	6	2
CB63	BIT	4,E	8	6	2
CB64	BIT	4,H	8	6	2
CB65	BIT	4,L	8	6	2
CB6E	BIT	5,(HL)	12	9	3
DDCBXX6E	BIT	5,(IX+dis)	20	15	5
FDCBXX6E	BIT	5,(IY+dis)	20	15	5
CB6F	BIT	5,A	8	6	2
CB68	BIT	5,B	8	6	2
CB69	BIT	5,C	8	6	2
CB6A	BIT	5,D	8	6	2
CB6B	BIT	5,E	8	6	2
CB6C	BIT	5,H	8	6	2
CB6D	BIT	5,L	8	6	2
CB76	BIT	6,(HL)	12	9	3
DDCBXX76	BIT	6,(IX+dis)	20	15	5
FDCBXX76	BIT	6,(IY+dis)	20	15	5
CB77	BIT	6,A	8	6	2
CB70	BIT	6,B	8	6	2
CB71	BIT	6,C	8	6	2
CB72	BIT	6,D	8	6	2

opcode	instruction	arguments	Z80	Z180	eZ80
CB73	BIT	6,E	8	6	2
CB74	BIT	6,H	8	6	2
CB75	BIT	6,L	8	6	2
CB7E	BIT	7,(HL)	12	9	3
DDCBXX7E	BIT	7,(IX+dis)	20	15	5
FDCBXX7E	BIT	7,(IY+dis)	20	15	5
CB7F	BIT	7,A	8	6	2
CB78	BIT	7,B	8	6	2
CB79	BIT	7,C	8	6	2
CB7A	BIT	7,D	8	6	2
CB7B	BIT	7,E	8	6	2
CB7C	BIT	7,H	8	6	2
CB7D	BIT	7,L	8	6	2
CDXXXX	CALL	ADDR	17	16	7(5)
DCXXXX	CALL	C,ADDR	17/10	16/6	7(6)/4(3)
FCXXXX	CALL	M,ADDR	17/10	16/6	7(6)/4(3)
D4XXXX	CALL	NC,ADDR	17/10	16/6	7(6)/4(3)
C4XXXX	CALL	NZ,ADDR	17/10	16/6	7(6)/4(3)
F4XXXX	CALL	P,ADDR	17/10	16/6	7(6)/4(3)
ECXXXX	CALL	PE,ADDR	17/10	16/6	7(6)/4(3)
E4XXXX	CALL	PO,ADDR	17/10	16/6	7(6)/4(3)
CCXXXX	CALL	Z,ADDR	17/10	16/6	7(6)/4(3)
3F	CCF		4	3	1
BE	CP	(HL)	7	6	2
DDBEXX	CP	(IX+dis)	19	14	4
FDBEXX	CP	(IY+dis)	19	14	4
BF	CP	A	4	4	1
DDBC	CP	A,IXH			2
DDBD	CP	A,IXL			2
FDBC	CP	A,IYH			2
FDBD	CP	A,IYL			2
B8	CP	B	4	4	1
B9	CP	C	4	4	1
BA	CP	D	4	4	1
BB	CP	E	4	4	1
BC	CP	H	4	4	1
BD	CP	L	4	4	1
FEXX	CP	NN	7	6	2
EDA9	CPD		16	12	3
EDB9	CPDR		21/16	14/12	1+3*BC
EDA1	CPI		16	12	3
EDB1	CPIR		21/16	14/12	1+3*BC
2F	CPL		4	3	1
27	DAA		4	4	1
35	DEC	(HL)	11	10	4
DD35XX	DEC	(IX+dis)	23	18	6
FD35XX	DEC	(IY+dis)	23	18	6
3D	DEC	A	4	4	1
05	DEC	B	4	4	1
0B	DEC	BC	6	4	1
0D	DEC	C	4	4	1
15	DEC	D	4	4	1
1B	DEC	DE	6	4	1
1D	DEC	E	4	4	1
25	DEC	H	4	4	1
2B	DEC	HL	6	4	1
DD2B	DEC	IX	10	7	2
DD25	DEC	IXH			2
DD2D	DEC	IXL			2
FD2B	DEC	IY	10	7	2
FD25	DEC	IYH			2
FD2D	DEC	IYL			2
2D	DEC	L	4	4	1
3B	DEC	SP	6	4	1
F3	DI		4	3	1
10XX	DJNZ	dis	13/8	9/7	4/2
FB	EI		4	3	1
E3	EX	(SP),HL	19	16	7(5)

opcode	instruction	arguments	Z80	Z180	eZ80
DDE3	EX	(SP),IX	23	19	8(6)
FDE3	EX	(SP),IY	23	19	8(6)
08	EX	AF,A'F'	4	4	1
EB	EX	DE,HL	4	3	1
D9	EXX		4	3	1
76	HALT		4	3	1
ED46	IM	0	8	6	2
ED56	IM	1	8	6	2
ED5E	IM	2	8	6	2
ED78	IN	A,(C)	12	9	3
DBXX	IN	A,(port)	11	9	3
ED40	IN	B,(C)	12	9	3
ED48	IN	C,(C)	12	9	3
ED50	IN	D,(C)	12	9	3
ED58	IN	E,(C)	12	9	3
ED60	IN	H,(C)	12	9	3
ED68	IN	L,(C)	12	9	3
ED38XX	IN0	A,(port)		12	4
ED00XX	IN0	B,(port)		12	4
ED08XX	IN0	C,(port)		12	4
ED10XX	IN0	D,(port)		12	4
ED18XX	IN0	E,(port)		12	4
ED20XX	IN0	H,(port)		12	4
ED28XX	IN0	L,(port)		12	4
34	INC	(HL)	11	10	4
DD34XX	INC	(IX+dis)	23	18	6
FD34XX	INC	(IY+dis)	23	18	6
3C	INC	A	4	4	1
04	INC	B	4	4	1
03	INC	BC	6	4	1
0C	INC	C	4	4	1
14	INC	D	4	4	1
13	INC	DE	6	4	1
1C	INC	E	4	4	1
24	INC	H	4	4	1
23	INC	HL	6	4	1
DD23	INC	IX	6	7	2
DD24	INC	IXH			2
DD2C	INC	IXL			2
FD23	INC	IY	6	7	2
FD24	INC	IYH			2
FD2C	INC	IYL			2
2C	INC	L	4	4	1
33	INC	SP	6	4	1
EDAA	IND		16	12	5
ED8C	IND2				5
ED9C	IND2R				2+3*BC
ED8A	INDM				5
ED9A	INDMR				2+3*BC
EDBA	INDR		21/16	14/12	2+3*BC
EDCA	INDRX				2+3*BC
EDA2	INI		16	12	5
ED84	INI2				5
ED94	INI2R				2+3*BC
ED82	INIM				5
ED92	INIMR				2+3*BC
EDB2	INIR		21/16	14/12	2+3*BC
EDC2	INIRX				2+3*BC
E9	JP	(HL)	4	3	3
DDE9	JP	(IX)	8	6	4
FDE9	JP	(IY)	8	6	4
C3XXXX	JP	ADDR	10	9	5(4)
DAXXXX	JP	C,ADDR	10	9/6	5(4)/4(3)
FAXXXX	JP	M,ADDR	10	9/6	5(4)/4(3)
D2XXXX	JP	NC,ADDR	10	9/6	5(4)/4(3)
C2XXXX	JP	NZ,ADDR	10	9/6	5(4)/4(3)
F2XXXX	JP	P,ADDR	10	9/6	5(4)/4(3)
EAXXXX	JP	PE,ADDR	10	9/6	5(4)/4(3)

opcode	instruction	arguments	Z80	Z180	eZ80
E2XXXX	JP	PO,ADDR	10	9/6	5(4)/4(3)
CAXXXX	JP	Z,ADDR	10	9/6	5(4)/4(3)
38XX	JR	C,dis	12/7	8/6	3/2
18XX	JR	dis	12	8	3
30XX	JR	NC,dis	12/7	8/6	3/2
20XX	JR	NZ,dis	12/7	8/6	3/2
28XX	JR	Z,dis	12/7	8/6	3/2
32XXXX	LD	(ADDR),A	13	13	5(4)
ED43XXXX	LD	(ADDR),BC	20	19	8(6)
ED53XXXX	LD	(ADDR),DE	20	19	8(6)
22XXXX	LD	(ADDR),HL	16	16	7(5)
ED63XXXX	LD	(ADDR),HL	20	19	8(6)
DD22XXXX	LD	(ADDR),IX	20	19	8(6)
FD22XXXX	LD	(ADDR),IY	20	19	8(6)
ED73XXXX	LD	(ADDR),SP	20	19	8(6)
02	LD	(BC),A	7	7	2
12	LD	(DE),A	7	7	2
77	LD	(HL),A	7	7	2
70	LD	(HL),B	7	7	2
ED0F	LD	(HL),BC			5(4)
71	LD	(HL),C	7	7	2
72	LD	(HL),D	7	7	2
ED1F	LD	(HL),DE			5(4)
73	LD	(HL),E	7	7	2
74	LD	(HL),H	7	7	2
ED2F	LD	(HL),HL			5(4)
ED3F	LD	(HL),IX			5(4)
ED3E	LD	(HL),IY			5(4)
75	LD	(HL),L	7	7	2
36XX	LD	(HL),NN	10	9	3
DD77XX	LD	(IX+dis),A	19	15	4
DD70XX	LD	(IX+dis),B	19	15	4
DD0FXX	LD	(IX+dis),BC			6(5)
DD71XX	LD	(IX+dis),C	19	15	4
DD72XX	LD	(IX+dis),D	19	15	4
DD1FXX	LD	(IX+dis),DE			6(5)
DD73XX	LD	(IX+dis),E	19	15	4
DD74XX	LD	(IX+dis),H	19	15	4
DD2FXX	LD	(IX+dis),HL			6(5)
DD3FXX	LD	(IX+dis),IX			6(5)
DD3EXX	LD	(IX+dis),IY			6(5)
DD75XX	LD	(IX+dis),L	19	15	4
DD36XXXX	LD	(IX+dis),NN	19	15	5
FD77XX	LD	(IY+dis),A	19	15	4
FD70XX	LD	(IY+dis),B	19	15	4
FD0FXX	LD	(IY+dis),BC			6(5)
FD71XX	LD	(IY+dis),C	19	15	4
FD72XX	LD	(IY+dis),D	19	15	4
FD1FXX	LD	(IY+dis),DE			6(5)
FD73XX	LD	(IY+dis),E	19	15	4
FD74XX	LD	(IY+dis),H	19	15	4
FD2FXX	LD	(IY+dis),HL			6(5)
FD3EXX	LD	(IY+dis),IX			6(5)
FD3FXX	LD	(IY+dis),IY			6(5)
FD75XX	LD	(IY+dis),L	19	15	4
FD36XXXX	LD	(IY+dis),NN	19	15	5
3AXXXX	LD	A,(ADDR)	13	12	5(4)
0A	LD	A,(BC)	7	6	2
1A	LD	A,(DE)	7	6	2
7E	LD	A,(HL)	7	6	2
DD7EXX	LD	A,(IX+dis)	19	14	4
FD7EXX	LD	A,(IY+dis)	19	14	4
7F	LD	A,A	4	4	1
78	LD	A,B	4	4	1
79	LD	A,C	4	4	1
7A	LD	A,D	4	4	1
7B	LD	A,E	4	4	1
7C	LD	A,H	4	4	1

opcode	instruction	arguments	Z80	Z180	eZ80
ED57	LD	A,I	9	6	2
DD7C	LD	A,IXH			2
DD7D	LD	A,IXL			2
FD7C	LD	A,IYH			2
FD7D	LD	A,IYL			2
7D	LD	A,L	4	4	1
ED6E	LD	A,MB			2
3EXX	LD	A,NN	7	6	2
ED5F	LD	A,R	9	6	2
46	LD	B,(HL)	7	6	2
DD46XX	LD	B,(IX+dis)	19	14	4
FD46XX	LD	B,(IY+dis)	19	14	4
47	LD	B,A	4	4	1
40	LD	B,B	4	4	
41	LD	B,C	4	4	1
42	LD	B,D	4	4	1
43	LD	B,E	4	4	1
44	LD	B,H	4	4	1
DD44	LD	B,IXH			2
DD45	LD	B,IXL			2
FD44	LD	B,IYH			2
FD45	LD	B,IYL			2
45	LD	B,L	4	4	1
06XX	LD	B,NN	7	6	2
ED4BXXXX	LD	BC,(ADDR)	20	18	8(6)
ED07	LD	BC,(HL)			5(4)
DD07XX	LD	BC,(IX+dis)			6(5)
FD07XX	LD	BC,(IY+dis)			6(5)
01XXXX	LD	BC,NNNN	10	9	4(3)
4E	LD	C,(HL)	7	6	2
DD4EXX	LD	C,(IX+dis)	19	14	4
FD4EXX	LD	C,(IY+dis)	19	14	4
4F	LD	C,A	4	4	1
48	LD	C,B	4	4	1
49	LD	C,C	4	4	
4A	LD	C,D	4	4	1
4B	LD	C,E	4	4	1
4C	LD	C,H	4	4	1
DD4C	LD	C,IXH			2
DD4D	LD	C,IXL			2
FD4C	LD	C,IYH			2
FD4D	LD	C,IYL			2
4D	LD	C,L	4	4	1
0EXX	LD	C,NN	7	6	2
56	LD	D,(HL)	7	6	2
DD56XX	LD	D,(IX+dis)	19	14	4
FD56XX	LD	D,(IY+dis)	19	14	4
57	LD	D,A	4	4	1
50	LD	D,B	4	4	1
51	LD	D,C	4	4	1
52	LD	D,D	4	4	
53	LD	D,E	4	4	1
54	LD	D,H	4	4	1
DD54	LD	D,IXH			2
DD55	LD	D,IXL			2
FD54	LD	D,IYH			2
FD55	LD	D,IYL			2
55	LD	D,L	4	4	1
16XX	LD	D,NN	7	6	2
ED5BXXXX	LD	DE,(ADDR)	20	18	8(6)
ED17	LD	DE,(HL)			5(4)
DD17XX	LD	DE,(IX+dis)			6(5)
FD17XX	LD	DE,(IY+dis)			6(5)
11XXXX	LD	DE,NNNN	10	9	4(3)
5E	LD	E,(HL)	7	6	2
DD5EXX	LD	E,(IX+dis)	19	14	4
FD5EXX	LD	E,(IY+dis)	19	14	4
5F	LD	E,A	4	4	1

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58	LD	E,B	4	4	1
59	LD	E,C	4	4	1
5A	LD	E,D	4	4	1
5B	LD	E,E	4	4	
5C	LD	E,H	4	4	1
DD5C	LD	E,IXH			2
DD5D	LD	E,IXL			2
FD5C	LD	E,IYH			2
FD5D	LD	E,IYL			2
5D	LD	E,L	4	4	1
1EXX	LD	E,NN	7	6	2
66	LD	H,(HL)	7	6	2
DD66XX	LD	H,(IX+dis)	19	14	4
FD66XX	LD	H,(IY+dis)	19	14	4
67	LD	H,A	4	4	1
60	LD	H,B	4	4	1
61	LD	H,C	4	4	1
62	LD	H,D	4	4	1
63	LD	H,E	4	4	1
64	LD	H,H	4	4	1
65	LD	H,L	4	4	1
26XX	LD	H,NN	7	6	2
2AXXXX	LD	HL,(ADDR)	16	15	7(5)
ED6BXXXX	LD	HL,(ADDR)	20	18	8(6)
ED27	LD	HL,(HL)			5(4)
DD27XX	LD	HL,(IX+dis)			6(5)
FD27XX	LD	HL,(IY+dis)			6(5)
EDD7	LD	HL,I			2
21XXXX	LD	HL,NNNN	10	9	4(3)
ED47	LD	I,A	9	6	2
EDC7	LD	I,HL			2
DD2AXXXX	LD	IX,(ADDR)	20	18	8(6)
ED37	LD	IX,(HL)			5(4)
DD37XX	LD	IX,(IX+dis)			6(5)
FD31XX	LD	IX,(IY+dis)			6(5)
DD21XXXX	LD	IX,NNNN	14	12	5(4)
DD67	LD	IXH,A			2
DD60	LD	IXH,B			2
DD61	LD	IXH,C			2
DD62	LD	IXH,D			2
DD63	LD	IXH,E			2
DD64	LD	IXH,IXH			2
DD65	LD	IXH,IXL			2
DD26XX	LD	IXH,NN			2
DD6F	LD	IXL,A			2
DD68	LD	IXL,B			2
DD69	LD	IXL,C			2
DD6A	LD	IXL,D			2
DD6B	LD	IXL,E			2
DD6C	LD	IXL,IXH			2
DD6D	LD	IXL,IXL			2
DD2EXX	LD	IXL,NN			2
FD2AXXXX	LD	IY,(ADDR)	20	18	8(6)
ED31	LD	IY,(HL)			5(4)
DD31XX	LD	IY,(IX+dis)			6(5)
FD37XX	LD	IY,(IY+dis)			6(5)
FD21XXXX	LD	IY,NNNN	14	12	5(4)
FD67	LD	IYH,A			2
FD60	LD	IYH,B			2
FD61	LD	IYH,C			2
FD62	LD	IYH,D			2
FD63	LD	IYH,E			2
FD64	LD	IYH,IYH			2
FD65	LD	IYH,IYL			2
FD26XX	LD	IYH,NN			2
FD6F	LD	IYL,A			2
FD68	LD	IYL,B			2
FD69	LD	IYL,C			2

opcode	instruction	arguments	Z80	Z180	eZ80
FD6A	LD	IYL,D			2
FD6B	LD	IYL,E			2
FD6C	LD	IYL,IYH			2
FD6D	LD	IYL,IYL			2
FD2EXX	LD	IYL,NN			2
6E	LD	L,(HL)	7	6	2
DD6EXX	LD	L,(IX+dis)	19	14	4
FD6EXX	LD	L,(IY+dis)	19	14	4
6F	LD	L,A	4	4	1
68	LD	L,B	4	4	1
69	LD	L,C	4	4	1
6A	LD	L,D	4	4	1
6B	LD	L,E	4	4	1
6C	LD	L,H	4	4	1
6D	LD	L,L	4	4	1
2EXX	LD	L,NN	7	6	2
ED6D	LD	MB,A			2
ED4F	LD	R,A	9	6	2
ED7BXXXX	LD	SP,(ADDR)	20	18	6(5)
F9	LD	SP,HL	6	4	1
DDF9	LD	SP,IX	10	7	2
DDF9	LD	SP,IY	10	7	2
31XXXX	LD	SP,NNNN	10	9	4(3)
EDA8	LDD		16	12	5
EDB8	LDDR		21/16	14/12	2+3*BC
EDA0	LDI		16	12	5
EDB0	LDIR		21/16	14/12	2+3*BC
ED02XX	LEA	BC,IX+dis			3
ED03XX	LEA	BC,IY+dis			3
ED12XX	LEA	DE,IX+dis			3
ED13XX	LEA	DE,IY+dis			3
ED22XX	LEA	HL,IX+dis			3
ED23XX	LEA	HL,IY+dis			3
ED32XX	LEA	IX,IX+dis			3
ED54XX	LEA	IX,IX+dis			3
ED55XX	LEA	IY,IX+dis			3
ED33XX	LEA	IY,IY+dis			3
ED4C	MLT	BC		17	6
ED5C	MLT	DE		17	6
ED6C	MLT	HL		17	6
ED7C	MLT	SP		17	6
ED44	NEG		8	6	2
00	NOP		4	3	1
B6	OR	(HL)	7	6	2
DDB6XX	OR	(IX+dis)	19	14	4
FDB6XX	OR	(IY+dis)	19	14	4
B7	OR	A	4	4	1
DDB4	OR	A,IXH			2
DDB5	OR	A,IXL			2
FDB4	OR	A,IYH			2
FDB5	OR	A,IYL			2
B0	OR	B	4	4	1
B1	OR	C	4	4	1
B2	OR	D	4	4	1
B3	OR	E	4	4	1
B4	OR	H	4	4	1
B5	OR	L	4	4	1
F6XX	OR	NN	7	6	2
EDBC	OTD2R				2+3*B
ED8B	OTDM			14	5
ED9B	OTDMR			16/14	2+3*B
EDBB	OTDR		21/16	14/12	2+3*B
EDCB	OTDRX				2+3*BC
EDB4	OTI2R				2+3*B
ED83	OTIM			14	5
ED93	OTIMR			16/14	2+3*B
EDB3	OTIR		21/16	14/12	2+3*B
EDC3	OTIRX				2+3*B

opcode	instruction	arguments	Z80	Z180	eZ80
ED79	OUT	(C),A	12	10	3
ED41	OUT	(C),B	12	10	3
ED49	OUT	(C),C	12	10	3
ED51	OUT	(C),D	12	10	3
ED59	OUT	(C),E	12	10	3
ED61	OUT	(C),H	12	10	3
ED69	OUT	(C),L	12	10	3
D3XX	OUT	(port),A	11	10	3
ED39XX	OUT0	(port),A		13	4
ED01XX	OUT0	(port),B		13	4
ED09XX	OUT0	(port),C		13	4
ED11XX	OUT0	(port),D		13	4
ED19XX	OUT0	(port),E		13	4
ED21XX	OUT0	(port),H		13	4
ED29XX	OUT0	(port),L		13	4
EDAB	OUTD		16	12	5
EDAC	OUTD2				5
EDA3	OUTI		16	12	5
EDA4	OUTI2				5
ED65XX	PEA	IX+dis			6(5)
ED66XX	PEA	IY+dis			6(5)
F1	POP	AF	10	9	4(3)
C1	POP	BC	10	9	4(3)
D1	POP	DE	10	9	4(3)
E1	POP	HL	10	9	4(3)
DDE1	POP	IX	14	12	5(4)
FDE1	POP	IY	14	12	5(4)
F5	PUSH	AF	11	11	4(3)
C5	PUSH	BC	11	11	4(3)
D5	PUSH	DE	11	11	4(3)
E5	PUSH	HL	11	11	4(3)
DDE5	PUSH	IX	15	14	5(4)
FDE5	PUSH	IY	15	14	5(4)
CB86	RES	0,(HL)	15	13	3
DDCBXX86	RES	0,(IX+dis)	23	19	5
FDCBXX86	RES	0,(IY+dis)	23	19	5
CB87	RES	0,A	8	7	2
CB80	RES	0,B	8	7	2
CB81	RES	0,C	8	7	2
CB82	RES	0,D	8	7	2
CB83	RES	0,E	8	7	2
CB84	RES	0,H	8	7	2
CB85	RES	0,L	8	7	2
CB8E	RES	1,(HL)	15	13	3
DDCBXX8E	RES	1,(IX+dis)	23	19	5
FDCBXX8E	RES	1,(IY+dis)	23	19	5
CB8F	RES	1,A	8	7	2
CB88	RES	1,B	8	7	2
CB89	RES	1,C	8	7	2
CB8A	RES	1,D	8	7	2
CB8B	RES	1,E	8	7	2
CB8C	RES	1,H	8	7	2
CB8D	RES	1,L	8	7	2
CB96	RES	2,(HL)	15	13	3
DDCBXX96	RES	2,(IX+dis)	23	19	5
FDCBXX96	RES	2,(IY+dis)	23	19	5
CB97	RES	2,A	8	7	2
CB90	RES	2,B	8	7	2
CB91	RES	2,C	8	7	2
CB92	RES	2,D	8	7	2
CB93	RES	2,E	8	7	2
CB94	RES	2,H	8	7	2
CB95	RES	2,L	8	7	2
CB9E	RES	3,(HL)	15	13	3
DDCBXX9E	RES	3,(IX+dis)	23	19	5
FDCBXX9E	RES	3,(IY+dis)	23	19	5
CB9F	RES	3,A	8	7	2
CB98	RES	3,B	8	7	2

opcode	instruction	arguments	Z80	Z180	eZ80
CB99	RES	3,C	8	7	2
CB9A	RES	3,D	8	7	2
CB9B	RES	3,E	8	7	2
CB9C	RES	3,H	8	7	2
CB9D	RES	3,L	8	7	2
CBA6	RES	4,(HL)	15	13	3
DDCBXXA6	RES	4,(IX+dis)	23	19	5
FDCBXXA6	RES	4,(IY+dis)	23	19	5
CBA7	RES	4,A	8	7	2
CBA0	RES	4,B	8	7	2
CBA1	RES	4,C	8	7	2
CBA2	RES	4,D	8	7	2
CBA3	RES	4,E	8	7	2
CBA4	RES	4,H	8	7	2
CBA5	RES	4,L	8	7	2
CBAE	RES	5,(HL)	15	13	3
DDCBXXAE	RES	5,(IX+dis)	23	19	5
FDCBXXAE	RES	5,(IY+dis)	23	19	5
CBAF	RES	5,A	8	7	2
CBA8	RES	5,B	8	7	2
CBA9	RES	5,C	8	7	2
CBAA	RES	5,D	8	7	2
CBAB	RES	5,E	8	7	2
CBAC	RES	5,H	8	7	2
CBAD	RES	5,L	8	7	2
CBB6	RES	6,(HL)	15	13	3
DDCBXXB6	RES	6,(IX+dis)	23	19	5
FDCBXXB6	RES	6,(IY+dis)	23	19	5
CBB7	RES	6,A	8	7	2
CBB0	RES	6,B	8	7	2
CBB1	RES	6,C	8	7	2
CBB2	RES	6,D	8	7	2
CBB3	RES	6,E	8	7	2
CBB4	RES	6,H	8	7	2
CBB5	RES	6,L	8	7	2
CBBE	RES	7,(HL)	15	13	3
DDCBXXBE	RES	7,(IX+dis)	23	19	5
FDCBXXBE	RES	7,(IY+dis)	23	19	5
CBBF	RES	7,A	8	7	2
CBB8	RES	7,B	8	7	2
CBB9	RES	7,C	8	7	2
CBBA	RES	7,D	8	7	2
CBBB	RES	7,E	8	7	2
CBBC	RES	7,H	8	7	2
CBBD	RES	7,L	8	7	2
D8	RET	C	11/5	10/5	7(6)/2
F8	RET	M	11/5	10/5	7(6)/2
D0	RET	NC	11/5	10/5	7(6)/2
C0	RET	NZ	11/5	10/5	7(6)/2
F0	RET	P	11/5	10/5	7(6)/2
E8	RET	PE	11/5	10/5	7(6)/2
E0	RET	PO	11/5	10/5	7(6)/2
C8	RET	Z	11/5	10/5	7(6)/2
C9	RET		10	9	6(5)
ED4D	RETI		14	12/22	7(6)
ED45	RETN		14	12	7(6)
CB16	RL	(HL)	15	13	5
DDCBXX16	RL	(IX+dis)	23	19	7
FDCBXX16	RL	(IY+dis)	23	19	7
CB17	RL	A	8	7	2
CB10	RL	B	8	7	2
CB11	RL	C	8	7	2
CB12	RL	D	8	7	2
CB13	RL	E	8	7	2
CB14	RL	H	8	7	2
CB15	RL	L	8	7	2
17	RLA		4	3	1
CB06	RLC	(HL)	15	13	5

opcode	instruction	arguments	Z80	Z180	eZ80
DDCBXX06	RLC	(IX+dis)	23	19	7
FDCBXX06	RLC	(IY+dis)	23	19	7
CB07	RLC	A	8	7	2
CB00	RLC	B	8	7	2
CB01	RLC	C	8	7	2
CB02	RLC	D	8	7	2
CB03	RLC	E	8	7	2
CB04	RLC	H	8	7	2
CB05	RLC	L	8	7	2
07	RLCA		4	3	1
ED6F	RLD		18	16	5
CB1E	RR	(HL)	15	13	5
DDCBXX1E	RR	(IX+dis)	23	19	7
FDCBXX1E	RR	(IY+dis)	23	19	7
CB1F	RR	A	8	7	2
CB18	RR	B	8	7	2
CB19	RR	C	8	7	2
CB1A	RR	D	8	7	2
CB1B	RR	E	8	7	2
CB1C	RR	H	8	7	2
CB1D	RR	L	8	7	2
1F	RRA		4	3	1
CB0E	RRC	(HL)	15	13	5
DDCBXX0E	RRC	(IX+dis)	23	19	7
FDCBXX0E	RRC	(IY+dis)	23	19	7
CB0F	RRC	A	8	7	2
CB08	RRC	B	8	7	2
CB09	RRC	C	8	7	2
CB0A	RRC	D	8	7	2
CB0B	RRC	E	8	7	2
CB0C	RRC	H	8	7	2
CB0D	RRC	L	8	7	2
0F	RRCA		4	3	1
ED67	RRD		18	16	5
ED7E	RSMIX				2
C7	RST	00H	11	11	6(5)
CF	RST	08H	11	11	6(5)
D7	RST	10H	11	11	6(5)
DF	RST	18H	11	11	6(5)
E7	RST	20H	11	11	6(5)
EF	RST	28H	11	11	6(5)
F7	RST	30H	11	11	6(5)
FF	RST	38H	11	11	6(5)
9E	SBC	A,(HL)	7	6	2
DD9EXX	SBC	A,(IX+dis)	19	14	4
FD9EXX	SBC	A,(IY+dis)	19	14	4
9F	SBC	A,A	4	4	1
98	SBC	A,B	4	4	1
99	SBC	A,C	4	4	1
9A	SBC	A,D	4	4	1
9B	SBC	A,E	4	4	1
9C	SBC	A,H	4	4	1
DD9C	SBC	A,IXH			2
DD9D	SBC	A,IXL			2
FD9C	SBC	A,IYH			2
FD9D	SBC	A,IYL			2
9D	SBC	A,L	4	4	1
DEXX	SBC	A,NN	7	6	2
ED42	SBC	HL,BC	15	10	2
ED52	SBC	HL,DE	15	10	2
ED62	SBC	HL,HL	15	10	2
ED72	SBC	HL,SP	15	10	2
37	SCF		4	3	1
CBC6	SET	0,(HL)	15	13	3
DDCBXXC6	SET	0,(IX+dis)	23	19	5
FDCBXXC6	SET	0,(IY+dis)	23	19	5
CBC7	SET	0,A	8	7	2
CBC0	SET	0,B	8	7	2

opcode	instruction	arguments	Z80	Z180	eZ80
CBC1	SET	0,C	8	7	2
CBC2	SET	0,D	8	7	2
CBC3	SET	0,E	8	7	2
CBC4	SET	0,H	8	7	2
CBC5	SET	0,L	8	7	2
CBCE	SET	1,(HL)	15	13	3
DDCBXXCE	SET	1,(IX+dis)	23	19	5
FDCBXXCE	SET	1,(IY+dis)	23	19	5
CBCF	SET	1,A	8	7	2
CBC8	SET	1,B	8	7	2
CBC9	SET	1,C	8	7	2
CBCA	SET	1,D	8	7	2
CBCB	SET	1,E	8	7	2
CBCC	SET	1,H	8	7	2
CBCD	SET	1,L	8	7	2
CBD6	SET	2,(HL)	15	13	3
DDCBXXD6	SET	2,(IX+dis)	23	19	5
FDCBXXD6	SET	2,(IY+dis)	23	19	5
CBD7	SET	2,A	8	7	2
CBD0	SET	2,B	8	7	2
CBD1	SET	2,C	8	7	2
CBD2	SET	2,D	8	7	2
CBD3	SET	2,E	8	7	2
CBD4	SET	2,H	8	7	2
CBD5	SET	2,L	8	7	2
CBDE	SET	3,(HL)	15	13	3
DDCBXXDE	SET	3,(IX+dis)	23	19	5
FDCBXXDE	SET	3,(IY+dis)	23	19	5
CBDF	SET	3,A	8	7	2
CBD8	SET	3,B	8	7	2
CBD9	SET	3,C	8	7	2
CBD A	SET	3,D	8	7	2
CBDB	SET	3,E	8	7	2
CBDC	SET	3,H	8	7	2
CBDD	SET	3,L	8	7	2
CBE6	SET	4,(HL)	15	13	3
DDCBXXE6	SET	4,(IX+dis)	23	19	5
FDCBXXE6	SET	4,(IY+dis)	23	19	5
CBE7	SET	4,A	8	7	2
CBE0	SET	4,B	8	7	2
CBE1	SET	4,C	8	7	2
CBE2	SET	4,D	8	7	2
CBE3	SET	4,E	8	7	2
CBE4	SET	4,H	8	7	2
CBE5	SET	4,L	8	7	2
CBEE	SET	5,(HL)	15	13	3
DDCBXXEE	SET	5,(IX+dis)	23	19	5
FDCBXXEE	SET	5,(IY+dis)	23	19	5
CBEF	SET	5,A	8	7	2
CBE8	SET	5,B	8	7	2
CBE9	SET	5,C	8	7	2
CBEA	SET	5,D	8	7	2
CBEB	SET	5,E	8	7	2
CBEC	SET	5,H	8	7	2
CBED	SET	5,L	8	7	2
CBF6	SET	6,(HL)	15	13	3
DDCBXXF6	SET	6,(IX+dis)	23	19	5
FDCBXXF6	SET	6,(IY+dis)	23	19	5
CBF7	SET	6,A	8	7	2
CBF0	SET	6,B	8	7	2
CBF1	SET	6,C	8	7	2
CBF2	SET	6,D	8	7	2
CBF3	SET	6,E	8	7	2
CBF4	SET	6,H	8	7	2
CBF5	SET	6,L	8	7	2
CBFE	SET	7,(HL)	15	13	3
DDCBXXFE	SET	7,(IX+dis)	23	19	5
FDCBXXFE	SET	7,(IY+dis)	23	19	5

opcode	instruction	arguments	Z80	Z180	eZ80
CBFF	SET	7,A	8	7	2
CBF8	SET	7,B	8	7	2
CBF9	SET	7,C	8	7	2
CBFA	SET	7,D	8	7	2
CBFB	SET	7,E	8	7	2
CBFC	SET	7,H	8	7	2
CBFD	SET	7,L	8	7	2
CB26	SLA	(HL)	15	13	5
DDCBXX26	SLA	(IX+dis)	23	19	7
FDCBXX26	SLA	(IY+dis)	23	19	7
CB27	SLA	A	8	7	2
CB20	SLA	B	8	7	2
CB21	SLA	C	8	7	2
CB22	SLA	D	8	7	2
CB23	SLA	E	8	7	2
CB24	SLA	H	8	7	2
CB25	SLA	L	8	7	2
ED76	SLP			8	2
CB2E	SRA	(HL)	15	13	5
DDCBXX2E	SRA	(IX+dis)	23	19	7
FDCBXX2E	SRA	(IY+dis)	23	19	7
CB2F	SRA	A	8	7	2
CB28	SRA	B	8	7	2
CB29	SRA	C	8	7	2
CB2A	SRA	D	8	7	2
CB2B	SRA	E	8	7	2
CB2C	SRA	H	8	7	2
CB2D	SRA	L	8	7	2
CB3E	SRL	(HL)	15	13	5
DDCBXX3E	SRL	(IX+dis)	23	19	7
FDCBXX3E	SRL	(IY+dis)	23	19	7
CB3F	SRL	A	8	7	2
CB38	SRL	B	8	7	2
CB39	SRL	C	8	7	2
CB3A	SRL	D	8	7	2
CB3B	SRL	E	8	7	2
CB3C	SRL	H	8	7	2
CB3D	SRL	L	8	7	2
ED7D	STMIX				2
96	SUB	(HL)	7	6	2
DD96XX	SUB	(IX+dis)	19	14	4

opcode	instruction	arguments	Z80	Z180	eZ80
FD96XX	SUB	(IY+dis)	19	14	4
97	SUB	A	4	4	1
DD94	SUB	A,IXH			2
DD95	SUB	A,IXL			2
FD94	SUB	A,IYH			2
FD95	SUB	A,IYL			2
90	SUB	B	4	4	1
91	SUB	C	4	4	1
92	SUB	D	4	4	1
93	SUB	E	4	4	1
94	SUB	H	4	4	1
95	SUB	L	4	4	1
D6XX	SUB	NN	7	6	2
ED34	TST	(HL)		10	3
ED3C	TST	A		7	2
ED04	TST	B		7	2
ED0C	TST	C		7	2
ED14	TST	D		7	2
ED1C	TST	E		7	2
ED24	TST	H		7	2
ED2C	TST	L		7	2
ED64XX	TST	NN		9	3
ED74XX	TSTIO	NN		12	4
AE	XOR	(HL)	7	6	2
DDAEXX	XOR	(IX+dis)	19	14	4
FDAEXX	XOR	(IY+dis)	19	14	4
AF	XOR	A	4	4	1
DDAC	XOR	A,IXH			2
DDAD	XOR	A,IXL			2
FDAC	XOR	A,IYH			2
FDAD	XOR	A,IYL			2
A8	XOR	B	4	4	1
A9	XOR	C	4	4	1
AA	XOR	D	4	4	1
AB	XOR	E	4	4	1
AC	XOR	H	4	4	1
AD	XOR	L	4	4	1
EEXX	XOR	NN	7	6	2

Z-80 opcode table

opcode	instruction	arguments	Z80	Z180	eZ80
00	NOP		4	3	1
01XXXX	LD	BC,NNNN	10	9	4(3)
02	LD	(BC),A	7	7	2
03	INC	BC	6	4	1
04	INC	B	4	4	1
05	DEC	B	4	4	1
06XX	LD	B,NN	7	6	2
07	RLCA		4	3	1
08	EX	AF,A'F'	4	4	1
09	ADD	HL,BC	11	7	1
0A	LD	A,(BC)	7	6	2
0B	DEC	BC	6	4	1
0C	INC	C	4	4	1
0D	DEC	C	4	4	1
0EXX	LD	C,NN	7	6	2
0F	RRCA		4	3	1
10XX	DJNZ	dis	13/8	9/7	4/2
11XXXX	LD	DE,NNNN	10	9	4(3)
12	LD	(DE),A	7	7	2
13	INC	DE	6	4	1
14	INC	D	4	4	1
15	DEC	D	4	4	1
16XX	LD	D,NN	7	6	2
17	RLA		4	3	1
18XX	JR	dis	12	8	3
19	ADD	HL,DE	11	7	1
1A	LD	A,(DE)	7	6	2
1B	DEC	DE	6	4	1
1C	INC	E	4	4	1
1D	DEC	E	4	4	1
1EXX	LD	E,NN	7	6	2
1F	RRA		4	3	1
20XX	JR	NZ,dis	12/7	8/6	3/2
21XXXX	LD	HL,NNNN	10	9	4(3)
22XXXX	LD	(ADDR),HL	16	16	7(5)
23	INC	HL	6	4	1
24	INC	H	4	4	1
25	DEC	H	4	4	1
26XX	LD	H,NN	7	6	2
27	DAA		4	4	1
28XX	JR	Z,dis	12/7	8/6	3/2
29	ADD	HL,HL	11	7	1
2AXXXX	LD	HL,(ADDR)	16	15	7(5)
2B	DEC	HL	6	4	1
2C	INC	L	4	4	1
2D	DEC	L	4	4	1
2EXX	LD	L,NN	7	6	2
2F	CPL		4	3	1
30XX	JR	NC,dis	12/7	8/6	3/2
31XXXX	LD	SP,NNNN	10	9	4(3)
32XXXX	LD	(ADDR),A	13	13	5(4)
33	INC	SP	6	4	1
34	INC	(HL)	11	10	4
35	DEC	(HL)	11	10	4
36XX	LD	(HL),NN	10	9	3
37	SCF		4	3	1
38XX	JR	C,dis	12/7	8/6	3/2
39	ADD	HL,SP	11	7	1
3AXXXX	LD	A,(ADDR)	13	12	5(4)
3B	DEC	SP	6	4	1
3C	INC	A	4	4	1
3D	DEC	A	4	4	1
3EXX	LD	A,NN	7	6	2
3F	CCF		4	3	1
40	.SIS				1(2)
40	LD	B,B	4	4	

opcode	instruction	arguments	Z80	Z180	eZ80
41	LD	B,C	4	4	1
42	LD	B,D	4	4	1
43	LD	B,E	4	4	1
44	LD	B,H	4	4	1
45	LD	B,L	4	4	1
46	LD	B,(HL)	7	6	2
47	LD	B,A	4	4	1
48	LD	C,B	4	4	1
49	.LIS				1(2)
49	LD	C,C	4	4	
4A	LD	C,D	4	4	1
4B	LD	C,E	4	4	1
4C	LD	C,H	4	4	1
4D	LD	C,L	4	4	1
4E	LD	C,(HL)	7	6	2
4F	LD	C,A	4	4	1
50	LD	D,B	4	4	1
51	LD	D,C	4	4	1
52	.SIL				1(2)
52	LD	D,D	4	4	
53	LD	D,E	4	4	1
54	LD	D,H	4	4	1
55	LD	D,L	4	4	1
56	LD	D,(HL)	7	6	2
57	LD	D,A	4	4	1
58	LD	E,B	4	4	1
59	LD	E,C	4	4	1
5A	LD	E,D	4	4	1
5B	.LIL				1(2)
5B	LD	E,E	4	4	
5C	LD	E,H	4	4	1
5D	LD	E,L	4	4	1
5E	LD	E,(HL)	7	6	2
5F	LD	E,A	4	4	1
60	LD	H,B	4	4	1
61	LD	H,C	4	4	1
62	LD	H,D	4	4	1
63	LD	H,E	4	4	1
64	LD	H,H	4	4	1
65	LD	H,L	4	4	1
66	LD	H,(HL)	7	6	2
67	LD	H,A	4	4	1
68	LD	L,B	4	4	1
69	LD	L,C	4	4	1
6A	LD	L,D	4	4	1
6B	LD	L,E	4	4	1
6C	LD	L,H	4	4	1
6D	LD	L,L	4	4	1
6E	LD	L,(HL)	7	6	2
6F	LD	L,A	4	4	1
70	LD	(HL),B	7	7	2
71	LD	(HL),C	7	7	2
72	LD	(HL),D	7	7	2
73	LD	(HL),E	7	7	2
74	LD	(HL),H	7	7	2
75	LD	(HL),L	7	7	2
76	HALT		4	3	1
77	LD	(HL),A	7	7	2
78	LD	A,B	4	4	1
79	LD	A,C	4	4	1
7A	LD	A,D	4	4	1
7B	LD	A,E	4	4	1
7C	LD	A,H	4	4	1
7D	LD	A,L	4	4	1
7E	LD	A,(HL)	7	6	2
7F	LD	A,A	4	4	1

opcode	instruction	arguments	Z80	Z180	eZ80
80	ADD	A,B	4	4	1
81	ADD	A,C	4	4	1
82	ADD	A,D	4	4	1
83	ADD	A,E	4	4	1
84	ADD	A,H	4	4	1
85	ADD	A,L	4	4	1
86	ADD	A,(HL)	7	6	2
87	ADD	A,A	4	4	1
88	ADC	A,B	4	4	1
89	ADC	A,C	4	4	1
8A	ADC	A,D	4	4	1
8B	ADC	A,E	4	4	1
8C	ADC	A,H	4	4	1
8D	ADC	A,L	4	4	1
8E	ADC	A,(HL)	7	6	2
8F	ADC	A,A	4	4	1
90	SUB	B	4	4	1
91	SUB	C	4	4	1
92	SUB	D	4	4	1
93	SUB	E	4	4	1
94	SUB	H	4	4	1
95	SUB	L	4	4	1
96	SUB	(HL)	7	6	2
97	SUB	A	4	4	1
98	SBC	A,B	4	4	1
99	SBC	A,C	4	4	1
9A	SBC	A,D	4	4	1
9B	SBC	A,E	4	4	1
9C	SBC	A,H	4	4	1
9D	SBC	A,L	4	4	1
9E	SBC	A,(HL)	7	6	2
9F	SBC	A,A	4	4	1
A0	AND	B	4	4	1
A1	AND	C	4	4	1
A2	AND	D	4	4	1
A3	AND	E	4	4	1
A4	AND	H	4	4	1
A5	AND	L	4	4	1
A6	AND	(HL)	7	6	2
A7	AND	A	4	4	1
A8	XOR	B	4	4	1
A9	XOR	C	4	4	1
AA	XOR	D	4	4	1
AB	XOR	E	4	4	1
AC	XOR	H	4	4	1
AD	XOR	L	4	4	1
AE	XOR	(HL)	7	6	2
AF	XOR	A	4	4	1
B0	OR	B	4	4	1
B1	OR	C	4	4	1
B2	OR	D	4	4	1
B3	OR	E	4	4	1
B4	OR	H	4	4	1
B5	OR	L	4	4	1
B6	OR	(HL)	7	6	2
B7	OR	A	4	4	1
B8	CP	B	4	4	1
B9	CP	C	4	4	1
BA	CP	D	4	4	1
BB	CP	E	4	4	1
BC	CP	H	4	4	1
BD	CP	L	4	4	1
BE	CP	(HL)	7	6	2
BF	CP	A	4	4	1
C0	RET	NZ	11/5	10/5	7(6)/2
C1	POP	BC	10	9	4(3)
C2XXXX	JP	NZ,ADDR	10	9/6	5(4)/4(3)
C3XXXX	JP	ADDR	10	9	5(4)

opcode	instruction	arguments	Z80	Z180	eZ80
C4XXXX	CALL	NZ,ADDR	17/10	16/6	7(6)/4(3)
C5	PUSH	BC	11	11	4(3)
C6XX	ADD	A,NN	7	6	2
C7	RST	00H	11	11	6(5)
C8	RET	Z	11/5	10/5	7(6)/2
C9	RET		10	9	6(5)
CAXXXX	JP	Z,ADDR	10	9/6	5(4)/4(3)
CB00	RLC	B	8	7	2
CB01	RLC	C	8	7	2
CB02	RLC	D	8	7	2
CB03	RLC	E	8	7	2
CB04	RLC	H	8	7	2
CB05	RLC	L	8	7	2
CB06	RLC	(HL)	15	13	5
CB07	RLC	A	8	7	2
CB08	RRC	B	8	7	2
CB09	RRC	C	8	7	2
CB0A	RRC	D	8	7	2
CB0B	RRC	E	8	7	2
CB0C	RRC	H	8	7	2
CB0D	RRC	L	8	7	2
CB0E	RRC	(HL)	15	13	5
CB0F	RRC	A	8	7	2
CB10	RL	B	8	7	2
CB11	RL	C	8	7	2
CB12	RL	D	8	7	2
CB13	RL	E	8	7	2
CB14	RL	H	8	7	2
CB15	RL	L	8	7	2
CB16	RL	(HL)	15	13	5
CB17	RL	A	8	7	2
CB18	RR	B	8	7	2
CB19	RR	C	8	7	2
CB1A	RR	D	8	7	2
CB1B	RR	E	8	7	2
CB1C	RR	H	8	7	2
CB1D	RR	L	8	7	2
CB1E	RR	(HL)	15	13	5
CB1F	RR	A	8	7	2
CB20	SLA	B	8	7	2
CB21	SLA	C	8	7	2
CB22	SLA	D	8	7	2
CB23	SLA	E	8	7	2
CB24	SLA	H	8	7	2
CB25	SLA	L	8	7	2
CB26	SLA	(HL)	15	13	5
CB27	SLA	A	8	7	2
CB28	SRA	B	8	7	2
CB29	SRA	C	8	7	2
CB2A	SRA	D	8	7	2
CB2B	SRA	E	8	7	2
CB2C	SRA	H	8	7	2
CB2D	SRA	L	8	7	2
CB2E	SRA	(HL)	15	13	5
CB2F	SRA	A	8	7	2
CB38	SRL	B	8	7	2
CB39	SRL	C	8	7	2
CB3A	SRL	D	8	7	2
CB3B	SRL	E	8	7	2
CB3C	SRL	H	8	7	2
CB3D	SRL	L	8	7	2
CB3E	SRL	(HL)	15	13	5
CB3F	SRL	A	8	7	2
CB40	BIT	0,B	8	6	2
CB41	BIT	0,C	8	6	2
CB42	BIT	0,D	8	6	2
CB43	BIT	0,E	8	6	2
CB44	BIT	0,H	8	6	2

opcode	instruction	arguments	Z80	Z180	eZ80
CB45	BIT	0,L	8	6	2
CB46	BIT	0,(HL)	12	9	3
CB47	BIT	0,A	8	6	2
CB48	BIT	1,B	8	6	2
CB49	BIT	1,C	8	6	2
CB4A	BIT	1,D	8	6	2
CB4B	BIT	1,E	8	6	2
CB4C	BIT	1,H	8	6	2
CB4D	BIT	1,L	8	6	2
CB4E	BIT	1,(HL)	12	9	3
CB4F	BIT	1,A	8	6	2
CB50	BIT	2,B	8	6	2
CB51	BIT	2,C	8	6	2
CB52	BIT	2,D	8	6	2
CB53	BIT	2,E	8	6	2
CB54	BIT	2,H	8	6	2
CB55	BIT	2,L	8	6	2
CB56	BIT	2,(HL)	12	9	3
CB57	BIT	2,A	8	6	2
CB58	BIT	3,B	8	6	2
CB59	BIT	3,C	8	6	2
CB5A	BIT	3,D	8	6	2
CB5B	BIT	3,E	8	6	2
CB5C	BIT	3,H	8	6	2
CB5D	BIT	3,L	8	6	2
CB5E	BIT	3,(HL)	12	9	3
CB5F	BIT	3,A	8	6	2
CB60	BIT	4,B	8	6	2
CB61	BIT	4,C	8	6	2
CB62	BIT	4,D	8	6	2
CB63	BIT	4,E	8	6	2
CB64	BIT	4,H	8	6	2
CB65	BIT	4,L	8	6	2
CB66	BIT	4,(HL)	12	9	3
CB67	BIT	4,A	8	6	2
CB68	BIT	5,B	8	6	2
CB69	BIT	5,C	8	6	2
CB6A	BIT	5,D	8	6	2
CB6B	BIT	5,E	8	6	2
CB6C	BIT	5,H	8	6	2
CB6D	BIT	5,L	8	6	2
CB6E	BIT	5,(HL)	12	9	3
CB6F	BIT	5,A	8	6	2
CB70	BIT	6,B	8	6	2
CB71	BIT	6,C	8	6	2
CB72	BIT	6,D	8	6	2
CB73	BIT	6,E	8	6	2
CB74	BIT	6,H	8	6	2
CB75	BIT	6,L	8	6	2
CB76	BIT	6,(HL)	12	9	3
CB77	BIT	6,A	8	6	2
CB78	BIT	7,B	8	6	2
CB79	BIT	7,C	8	6	2
CB7A	BIT	7,D	8	6	2
CB7B	BIT	7,E	8	6	2
CB7C	BIT	7,H	8	6	2
CB7D	BIT	7,L	8	6	2
CB7E	BIT	7,(HL)	12	9	3
CB7F	BIT	7,A	8	6	2
CB80	RES	0,B	8	7	2
CB81	RES	0,C	8	7	2
CB82	RES	0,D	8	7	2
CB83	RES	0,E	8	7	2
CB84	RES	0,H	8	7	2
CB85	RES	0,L	8	7	2
CB86	RES	0,(HL)	15	13	3
CB87	RES	0,A	8	7	2
CB88	RES	1,B	8	7	2

opcode	instruction	arguments	Z80	Z180	eZ80
CB89	RES	1,C	8	7	2
CB8A	RES	1,D	8	7	2
CB8B	RES	1,E	8	7	2
CB8C	RES	1,H	8	7	2
CB8D	RES	1,L	8	7	2
CB8E	RES	1,(HL)	15	13	3
CB8F	RES	1,A	8	7	2
CB90	RES	2,B	8	7	2
CB91	RES	2,C	8	7	2
CB92	RES	2,D	8	7	2
CB93	RES	2,E	8	7	2
CB94	RES	2,H	8	7	2
CB95	RES	2,L	8	7	2
CB96	RES	2,(HL)	15	13	3
CB97	RES	2,A	8	7	2
CB98	RES	3,B	8	7	2
CB99	RES	3,C	8	7	2
CB9A	RES	3,D	8	7	2
CB9B	RES	3,E	8	7	2
CB9C	RES	3,H	8	7	2
CB9D	RES	3,L	8	7	2
CB9E	RES	3,(HL)	15	13	3
CB9F	RES	3,A	8	7	2
CBA0	RES	4,B	8	7	2
CBA1	RES	4,C	8	7	2
CBA2	RES	4,D	8	7	2
CBA3	RES	4,E	8	7	2
CBA4	RES	4,H	8	7	2
CBA5	RES	4,L	8	7	2
CBA6	RES	4,(HL)	15	13	3
CBA7	RES	4,A	8	7	2
CBA8	RES	5,B	8	7	2
CBA9	RES	5,C	8	7	2
CBAA	RES	5,D	8	7	2
CBAB	RES	5,E	8	7	2
CBAC	RES	5,H	8	7	2
CBAD	RES	5,L	8	7	2
CBAE	RES	5,(HL)	15	13	3
CBAF	RES	5,A	8	7	2
CBB0	RES	6,B	8	7	2
CBB1	RES	6,C	8	7	2
CBB2	RES	6,D	8	7	2
CBB3	RES	6,E	8	7	2
CBB4	RES	6,H	8	7	2
CBB5	RES	6,L	8	7	2
CBB6	RES	6,(HL)	15	13	3
CBB7	RES	6,A	8	7	2
CBB8	RES	7,B	8	7	2
CBB9	RES	7,C	8	7	2
CBBA	RES	7,D	8	7	2
CBBB	RES	7,E	8	7	2
CBBC	RES	7,H	8	7	2
CBBD	RES	7,L	8	7	2
CBBE	RES	7,(HL)	15	13	3
CBBF	RES	7,A	8	7	2
CBC0	SET	0,B	8	7	2
CBC1	SET	0,C	8	7	2
CBC2	SET	0,D	8	7	2
CBC3	SET	0,E	8	7	2
CBC4	SET	0,H	8	7	2
CBC5	SET	0,L	8	7	2
CBC6	SET	0,(HL)	15	13	3
CBC7	SET	0,A	8	7	2
CBC8	SET	1,B	8	7	2
CBC9	SET	1,C	8	7	2
CBCA	SET	1,D	8	7	2
CBCB	SET	1,E	8	7	2
CBCC	SET	1,H	8	7	2

opcode	instruction	arguments	Z80	Z180	eZ80
CBCD	SET	1,L	8	7	2
CBCD	SET	1,(HL)	15	13	3
CBCF	SET	1,A	8	7	2
CBD0	SET	2,B	8	7	2
CBD1	SET	2,C	8	7	2
CBD2	SET	2,D	8	7	2
CBD3	SET	2,E	8	7	2
CBD4	SET	2,H	8	7	2
CBD5	SET	2,L	8	7	2
CBD6	SET	2,(HL)	15	13	3
CBD7	SET	2,A	8	7	2
CBD8	SET	3,B	8	7	2
CBD9	SET	3,C	8	7	2
CBDA	SET	3,D	8	7	2
CBDB	SET	3,E	8	7	2
CBDC	SET	3,H	8	7	2
CBDD	SET	3,L	8	7	2
CBDE	SET	3,(HL)	15	13	3
CBDF	SET	3,A	8	7	2
CBE0	SET	4,B	8	7	2
CBE1	SET	4,C	8	7	2
CBE2	SET	4,D	8	7	2
CBE3	SET	4,E	8	7	2
CBE4	SET	4,H	8	7	2
CBE5	SET	4,L	8	7	2
CBE6	SET	4,(HL)	15	13	3
CBE7	SET	4,A	8	7	2
CBE8	SET	5,B	8	7	2
CBE9	SET	5,C	8	7	2
CBEA	SET	5,D	8	7	2
CBEB	SET	5,E	8	7	2
CBEC	SET	5,H	8	7	2
CBED	SET	5,L	8	7	2
CBEE	SET	5,(HL)	15	13	3
CBEF	SET	5,A	8	7	2
CBF0	SET	6,B	8	7	2
CBF1	SET	6,C	8	7	2
CBF2	SET	6,D	8	7	2
CBF3	SET	6,E	8	7	2
CBF4	SET	6,H	8	7	2
CBF5	SET	6,L	8	7	2
CBF6	SET	6,(HL)	15	13	3
CBF7	SET	6,A	8	7	2
CBF8	SET	7,B	8	7	2
CBF9	SET	7,C	8	7	2
CBFA	SET	7,D	8	7	2
CBFB	SET	7,E	8	7	2
CBFC	SET	7,H	8	7	2
CBFD	SET	7,L	8	7	2
CBFE	SET	7,(HL)	15	13	3
CBFF	SET	7,A	8	7	2
CCXXXX	CALL	Z,ADDR	17/10	16/6	7(6)/4(3)
CDXXXX	CALL	ADDR	17	16	7(5)
CEXX	ADC	A,NN	7	6	2
CF	RST	08H	11	11	6(5)
D0	RET	NC	11/5	10/5	7(6)/2
D1	POP	DE	10	9	4(3)
D2XXXX	JP	NC,ADDR	10	9/6	5(4)/4(3)
D3XX	OUT	(port),A	11	10	3
D4XXXX	CALL	NC,ADDR	17/10	16/6	7(6)/4(3)
D5	PUSH	DE	11	11	4(3)
D6XX	SUB	NN	7	6	2
D7	RST	10H	11	11	6(5)
D8	RET	C	11/5	10/5	7(6)/2
D9	EXX		4	3	1
DAXXXX	JP	C,ADDR	10	9/6	5(4)/4(3)
DBXX	IN	A,(port)	11	9	3
DCXXXX	CALL	C,ADDR	17/10	16/6	7(6)/4(3)

opcode	instruction	arguments	Z80	Z180	eZ80
DD07XX	LD	BC,(IX+dis)			6(5)
DD09	ADD	IX,BC	15	10	2
DD0FXX	LD	(IX+dis),BC			6(5)
DD17XX	LD	DE,(IX+dis)			6(5)
DD19	ADD	IX,DE	15	10	2
DD1FXX	LD	(IX+dis),DE			6(5)
DD21XXXX	LD	IX,NNNN	14	12	5(4)
DD22XXXX	LD	(ADDR),IX	20	19	8(6)
DD23	INC	IX	6	7	2
DD24	INC	IXH			2
DD25	DEC	IXH			2
DD26XX	LD	IXH,NN			2
DD27XX	LD	HL,(IX+dis)			6(5)
DD29	ADD	IX,IX	15	10	2
DD2AXXXX	LD	IX,(ADDR)	20	18	8(6)
DD2B	DEC	IX	10	7	2
DD2C	INC	IXL			2
DD2D	DEC	IXL			2
DD2EXX	LD	IXL,NN			2
DD2FXX	LD	(IX+dis),HL			6(5)
DD31XX	LD	IY,(IX+dis)			6(5)
DD34XX	INC	(IX+dis)	23	18	6
DD35XX	DEC	(IX+dis)	23	18	6
DD36XXXX	LD	(IX+dis),NN	19	15	5
DD37XX	LD	IX,(IX+dis)			6(5)
DD39	ADD	IX,SP	15	10	2
DD3EXX	LD	(IX+dis),IY			6(5)
DD3FXX	LD	(IX+dis),IX			6(5)
DD44	LD	B,IXH			2
DD45	LD	B,IXL			2
DD46XX	LD	B,(IX+dis)	19	14	4
DD4C	LD	C,IXH			2
DD4D	LD	C,IXL			2
DD4EXX	LD	C,(IX+dis)	19	14	4
DD54	LD	D,IXH			2
DD55	LD	D,IXL			2
DD56XX	LD	D,(IX+dis)	19	14	4
DD5C	LD	E,IXH			2
DD5D	LD	E,IXL			2
DD5EXX	LD	E,(IX+dis)	19	14	4
DD60	LD	IXH,B			2
DD61	LD	IXH,C			2
DD62	LD	IXH,D			2
DD63	LD	IXH,E			2
DD64	LD	IXH,IXH			2
DD65	LD	IXH,IXL			2
DD66XX	LD	H,(IX+dis)	19	14	4
DD67	LD	IXH,A			2
DD68	LD	IXL,B			2
DD69	LD	IXL,C			2
DD6A	LD	IXL,D			2
DD6B	LD	IXL,E			2
DD6C	LD	IXL,IXH			2
DD6D	LD	IXL,IXL			2
DD6EXX	LD	L,(IX+dis)	19	14	4
DD6F	LD	IXL,A			2
DD70XX	LD	(IX+dis),B	19	15	4
DD71XX	LD	(IX+dis),C	19	15	4
DD72XX	LD	(IX+dis),D	19	15	4
DD73XX	LD	(IX+dis),E	19	15	4
DD74XX	LD	(IX+dis),H	19	15	4
DD75XX	LD	(IX+dis),L	19	15	4
DD77XX	LD	(IX+dis),A	19	15	4
DD7C	LD	A,IXH			2
DD7D	LD	A,IXL			2
DD7EXX	LD	A,(IX+dis)	19	14	4
DD84	ADD	A,IXH			2
DD85	ADD	A,IXL			2

opcode	instruction	arguments	Z80	Z180	eZ80
DD86XX	ADD	A,(IX+dis)	19	14	4
DD8C	ADC	A,IXH			2
DD8D	ADC	A,IXL			2
DD8EXX	ADC	A,(IX+dis)	19	14	4
DD94	SUB	A,IXH			2
DD95	SUB	A,IXL			2
DD96XX	SUB	(IX+dis)	19	14	4
DD9C	SBC	A,IXH			2
DD9D	SBC	A,IXL			2
DD9EXX	SBC	A,(IX+dis)	19	14	4
DDA4	AND	A,IXH			2
DDA5	AND	A,IXL			2
DDA6XX	AND	(IX+dis)	19	14	4
DDAC	XOR	A,IXH			2
DDAD	XOR	A,IXL			2
DDAEXX	XOR	(IX+dis)	19	14	4
DDB4	OR	A,IXH			2
DDB5	OR	A,IXL			2
DDB6XX	OR	(IX+dis)	19	14	4
DDBC	CP	A,IXH			2
DDBD	CP	A,IXL			2
DDBEXX	CP	(IX+dis)	19	14	4
DDCBXX06	RLC	(IX+dis)	23	19	7
DDCBXX0E	RRC	(IX+dis)	23	19	7
DDCBXX16	RL	(IX+dis)	23	19	7
DDCBXX1E	RR	(IX+dis)	23	19	7
DDCBXX26	SLA	(IX+dis)	23	19	7
DDCBXX2E	SRA	(IX+dis)	23	19	7
DDCBXX3E	SRL	(IX+dis)	23	19	7
DDCBXX46	BIT	0,(IX+dis)	20	15	5
DDCBXX4E	BIT	1,(IX+dis)	20	15	5
DDCBXX56	BIT	2,(IX+dis)	20	15	5
DDCBXX5E	BIT	3,(IX+dis)	20	15	5
DDCBXX67	BIT	4,(IX+dis)	20	15	5
DDCBXX6E	BIT	5,(IX+dis)	20	15	5
DDCBXX76	BIT	6,(IX+dis)	20	15	5
DDCBXX7E	BIT	7,(IX+dis)	20	15	5
DDCBXX86	RES	0,(IX+dis)	23	19	5
DDCBXX8E	RES	1,(IX+dis)	23	19	5
DDCBXX96	RES	2,(IX+dis)	23	19	5
DDCBXX9E	RES	3,(IX+dis)	23	19	5
DDCBXXA6	RES	4,(IX+dis)	23	19	5
DDCBXXAE	RES	5,(IX+dis)	23	19	5
DDCBXXB6	RES	6,(IX+dis)	23	19	5
DDCBXXBE	RES	7,(IX+dis)	23	19	5
DDCBXXC6	SET	0,(IX+dis)	23	19	5
DDCBXXCE	SET	1,(IX+dis)	23	19	5
DDCBXXD6	SET	2,(IX+dis)	23	19	5
DDCBXXDE	SET	3,(IX+dis)	23	19	5
DDCBXXE6	SET	4,(IX+dis)	23	19	5
DDCBXXEE	SET	5,(IX+dis)	23	19	5
DDCBXXF6	SET	6,(IX+dis)	23	19	5
DDCBXXFE	SET	7,(IX+dis)	23	19	5
DDE1	POP	IX	14	12	5(4)
DDE3	EX	(SP),IX	23	19	8(6)
DDE5	PUSH	IX	15	14	5(4)
DDE9	JP	(IX)	8	6	4
DDF9	LD	SP,IX	10	7	2
DEXX	SBC	A,NN	7	6	2
DF	RST	18H	11	11	6(5)
E0	RET	PO	11/5	10/5	7(6)/2
E1	POP	HL	10	9	4(3)
E2XXXX	JP	PO,ADDR	10	9/6	5(4)/4(3)
E3	EX	(SP),HL	19	16	7(5)
E4XXXX	CALL	PO,ADDR	17/10	16/6	7(6)/4(3)
E5	PUSH	HL	11	11	4(3)
E6XX	AND	NN	7	6	2
E7	RST	20H	11	11	6(5)

opcode	instruction	arguments	Z80	Z180	eZ80
E8	RET	PE	11/5	10/5	7(6)/2
E9	JP	(HL)	4	3	3
EAXXXX	JP	PE,ADDR	10	9/6	5(4)/4(3)
EB	EX	DE,HL	4	3	1
ECXXXX	CALL	PE,ADDR	17/10	16/6	7(6)/4(3)
ED00XX	IN0	B,(port)		12	4
ED01XX	OUT0	(port),B		13	4
ED02XX	LEA	BC,IX+dis			3
ED03XX	LEA	BC,IY+dis			3
ED04	TST	B		7	2
ED07	LD	BC,(HL)			5(4)
ED08XX	IN0	C,(port)		12	4
ED09XX	OUT0	(port),C		13	4
ED0C	TST	C		7	2
ED0F	LD	(HL),BC			5(4)
ED10XX	IN0	D,(port)		12	4
ED11XX	OUT0	(port),D		13	4
ED12XX	LEA	DE,IX+dis			3
ED13XX	LEA	DE,IY+dis			3
ED14	TST	D		7	2
ED17	LD	DE,(HL)			5(4)
ED18XX	IN0	E,(port)		12	4
ED19XX	OUT0	(port),E		13	4
ED1C	TST	E		7	2
ED1F	LD	(HL),DE			5(4)
ED20XX	IN0	H,(port)		12	4
ED21XX	OUT0	(port),H		13	4
ED22XX	LEA	HL,IX+dis			3
ED23XX	LEA	HL,IY+dis			3
ED24	TST	H		7	2
ED27	LD	HL,(HL)			5(4)
ED28XX	IN0	L,(port)		12	4
ED29XX	OUT0	(port),L		13	4
ED2C	TST	L		7	2
ED2F	LD	(HL),HL			5(4)
ED31	LD	IY,(HL)			5(4)
ED32XX	LEA	IX,IX+dis			3
ED33XX	LEA	IY,IY+dis			3
ED34	TST	(HL)		10	3
ED37	LD	IX,(HL)			5(4)
ED38XX	IN0	A,(port)		12	4
ED39XX	OUT0	(port),A		13	4
ED3C	TST	A		7	2
ED3E	LD	(HL),IY			5(4)
ED3F	LD	(HL),IX			5(4)
ED40	IN	B,(C)	12	9	3
ED41	OUT	(C),B	12	10	3
ED42	SBC	HL,BC	15	10	2
ED43XXXX	LD	(ADDR),BC	20	19	8(6)
ED44	NEG		8	6	2
ED45	RETN		14	12	7(6)
ED46	IM	0	8	6	2
ED47	LD	I,A	9	6	2
ED48	IN	C,(C)	12	9	3
ED49	OUT	(C),C	12	10	3
ED4A	ADC	HL,BC	15	10	2
ED4BXXXX	LD	BC,(ADDR)	20	18	8(6)
ED4C	MLT	BC		17	6
ED4D	RETI		14	12/22	7(6)
ED4F	LD	R,A	9	6	2
ED50	IN	D,(C)	12	9	3
ED51	OUT	(C),D	12	10	3
ED52	SBC	HL,DE	15	10	2
ED53XXXX	LD	(ADDR),DE	20	19	8(6)
ED54XX	LEA	IX,IX+dis			3
ED55XX	LEA	IY,IX+dis			3
ED56	IM	1	8	6	2
ED57	LD	A,I	9	6	2

opcode	instruction	arguments	Z80	Z180	eZ80
ED58	IN	E,(C)	12	9	3
ED59	OUT	(C),E	12	10	3
ED5A	ADC	HL,DE	15	10	2
ED5BXXXX	LD	DE,(ADDR)	20	18	8(6)
ED5C	MLT	DE		17	6
ED5E	IM	2	8	6	2
ED5F	LD	A,R	9	6	2
ED60	IN	H,(C)	12	9	3
ED61	OUT	(C),H	12	10	3
ED62	SBC	HL,HL	15	10	2
ED63XXXX	LD	(ADDR),HL	20	19	8(6)
ED64XX	TST	NN		9	3
ED65XX	PEA	IX+dis			6(5)
ED66XX	PEA	IY+dis			6(5)
ED67	RRD		18	16	5
ED68	IN	L,(C)	12	9	3
ED69	OUT	(C),L	12	10	3
ED6A	ADC	HL,HL	15	10	2
ED6BXXXX	LD	HL,(ADDR)	20	18	8(6)
ED6C	MLT	HL		17	6
ED6D	LD	MB,A			2
ED6E	LD	A,MB			2
ED6F	RLD		18	16	5
ED72	SBC	HL,SP	15	10	2
ED73XXXX	LD	(ADDR),SP	20	19	8(6)
ED74XX	TSTIO	NN		12	4
ED76	SLP			8	2
ED78	IN	A,(C)	12	9	3
ED79	OUT	(C),A	12	10	3
ED7A	ADC	HL,SP	15	10	2
ED7BXXXX	LD	SP,(ADDR)	20	18	6(5)
ED7C	MLT	SP		17	6
ED7D	STMIX				2
ED7E	RSMIX				2
ED82	INIM				5
ED83	OTIM			14	5
ED84	INI2				5
ED8A	INDM				5
ED8B	OTDM			14	5
ED8C	IND2				5
ED92	INIMR				2+3*BC
ED93	OTIMR			16/14	2+3*B
ED94	INI2R				2+3*BC
ED9A	INDMR				2+3*BC
ED9B	OTDMR			16/14	2+3*B
ED9C	IND2R				2+3*BC
EDA0	LDI		16	12	5
EDA1	CPI		16	12	3
EDA2	INI		16	12	5
EDA3	OUTI		16	12	5
EDA4	OUTI2				5
EDA8	LDD		16	12	5
EDA9	CPD		16	12	3
EDAA	IND		16	12	5
EDAB	OUTD		16	12	5
EDAC	OUTD2				5
EDB0	LDIR		21/16	14/12	2+3*BC
EDB1	CPIR		21/16	14/12	1+3*BC
EDB2	INIR		21/16	14/12	2+3*BC
EDB3	OTIR		21/16	14/12	2+3*B
EDB4	OTI2R				2+3*B
EDB8	LDDR		21/16	14/12	2+3*BC
EDB9	CPDR		21/16	14/12	1+3*BC
EDBA	INDR		21/16	14/12	2+3*BC
EDBB	OTDR		21/16	14/12	2+3*B
EDBC	OTD2R				2+3*B
EDC2	INIRX				2+3*BC
EDC3	OTIRX				2+3*B

opcode	instruction	arguments	Z80	Z180	eZ80
EDC7	LD	I,HL			2
EDCA	INDRX				2+3*BC
EDCB	OTDRX				2+3*BC
EDD7	LD	HL,I			2
EEEXX	XOR	NN	7	6	2
EF	RST	28H	11	11	6(5)
F0	RET	P	11/5	10/5	7(6)/2
F1	POP	AF	10	9	4(3)
F2XXXX	JP	P,ADDR	10	9/6	5(4)/4(3)
F3	DI		4	3	1
F4XXXX	CALL	P,ADDR	17/10	16/6	7(6)/4(3)
F5	PUSH	AF	11	11	4(3)
F6XX	OR	NN	7	6	2
F7	RST	30H	11	11	6(5)
F8	RET	M	11/5	10/5	7(6)/2
F9	LD	SP,HL	6	4	1
FAXXXX	JP	M,ADDR	10	9/6	5(4)/4(3)
FB	EI		4	3	1
FCXXXX	CALL	M,ADDR	17/10	16/6	7(6)/4(3)
FD07XX	LD	BC,(IY+dis)			6(5)
FD09	ADD	IY,BC	15	10	2
FD0FXX	LD	(IY+dis),BC			6(5)
FD17XX	LD	DE,(IY+dis)			6(5)
FD19	ADD	IY,DE	15	10	2
FD1FXX	LD	(IY+dis),DE			6(5)
FD21XXXX	LD	IY,NNNN	14	12	5(4)
FD22XXXX	LD	(ADDR),IY	20	19	8(6)
FD23	INC	IY	6	7	2
FD24	INC	IYH			2
FD25	DEC	IYH			2
FD26XX	LD	IYH,NN			2
FD27XX	LD	HL,(IY+dis)			6(5)
FD29	ADD	IY,IY	15	10	2
FD2AXXXX	LD	IY,(ADDR)	20	18	8(6)
FD2B	DEC	IY	10	7	2
FD2C	INC	IYL			2
FD2D	DEC	IYL			2
FD2EXX	LD	IYL,NN			2
FD2FXX	LD	(IY+dis),HL			6(5)
FD31XX	LD	IX,(IY+dis)			6(5)
FD34XX	INC	(IY+dis)	23	18	6
FD35XX	DEC	(IY+dis)	23	18	6
FD36XXXX	LD	(IY+dis),NN	19	15	5
FD37XX	LD	IY,(IY+dis)			6(5)
FD39	ADD	IY,SP	15	10	2
FD3EXX	LD	(IY+dis),IX			6(5)
FD3FXX	LD	(IY+dis),IY			6(5)
FD44	LD	B,IYH			2
FD45	LD	B,IYL			2
FD46XX	LD	B,(IY+dis)	19	14	4
FD4C	LD	C,IYH			2
FD4D	LD	C,IYL			2
FD4EXX	LD	C,(IY+dis)	19	14	4
FD54	LD	D,IYH			2
FD55	LD	D,IYL			2
FD56XX	LD	D,(IY+dis)	19	14	4
FD5C	LD	E,IYH			2
FD5D	LD	E,IYL			2
FD5EXX	LD	E,(IY+dis)	19	14	4
FD60	LD	IYH,B			2
FD61	LD	IYH,C			2
FD62	LD	IYH,D			2
FD63	LD	IYH,E			2
FD64	LD	IYH,IYH			2
FD65	LD	IYH,IYL			2
FD66XX	LD	H,(IY+dis)	19	14	4
FD67	LD	IYH,A			2
FD68	LD	IYL,B			2

opcode	instruction	arguments	Z80	Z180	eZ80
FD69	LD	IYL,C			2
FD6A	LD	IYL,D			2
FD6B	LD	IYL,E			2
FD6C	LD	IYL,IYH			2
FD6D	LD	IYL,IYL			2
FD6EXX	LD	L,(IY+dis)	19	14	4
FD6F	LD	IYL,A			2
FD70XX	LD	(IY+dis),B	19	15	4
FD71XX	LD	(IY+dis),C	19	15	4
FD72XX	LD	(IY+dis),D	19	15	4
FD73XX	LD	(IY+dis),E	19	15	4
FD74XX	LD	(IY+dis),H	19	15	4
FD75XX	LD	(IY+dis),L	19	15	4
FD77XX	LD	(IY+dis),A	19	15	4
FD7C	LD	A,IYH			2
FD7D	LD	A,IYL			2
FD7EXX	LD	A,(IY+dis)	19	14	4
FD84	ADD	A,IYH			2
FD85	ADD	A,IYL			2
FD86XX	ADD	A,(IY+dis)	19	14	4
FD8C	ADC	A,IYH			2
FD8D	ADC	A,IYL			2
FD8EXX	ADC	A,(IY+dis)	19	14	4
FD94	SUB	A,IYH			2
FD95	SUB	A,IYL			2
FD96XX	SUB	(IY+dis)	19	14	4
FD9C	SBC	A,IYH			2
FD9D	SBC	A,IYL			2
FD9EXX	SBC	A,(IY+dis)	19	14	4
FDA4	AND	A,IYH			2
FDA5	AND	A,IYL			2
FDA6XX	AND	(IY+dis)	19	14	4
FDAC	XOR	A,IYH			2
FDAD	XOR	A,IYL			2
FDAEXX	XOR	(IY+dis)	19	14	4
FDB4	OR	A,IYH			2
FDB5	OR	A,IYL			2
FDB6XX	OR	(IY+dis)	19	14	4
FDBC	CP	A,IYH			2
FDBD	CP	A,IYL			2
FDBEXX	CP	(IY+dis)	19	14	4

opcode	instruction	arguments	Z80	Z180	eZ80
FDCBXX06	RLC	(IY+dis)	23	19	7
FDCBXX0E	RRC	(IY+dis)	23	19	7
FDCBXX16	RL	(IY+dis)	23	19	7
FDCBXX1E	RR	(IY+dis)	23	19	7
FDCBXX26	SLA	(IY+dis)	23	19	7
FDCBXX2E	SRA	(IY+dis)	23	19	7
FDCBXX3E	SRL	(IY+dis)	23	19	7
FDCBXX46	BIT	0,(IY+dis)	20	15	5
FDCBXX4E	BIT	1,(IY+dis)	20	15	5
FDCBXX56	BIT	2,(IY+dis)	20	15	5
FDCBXX5E	BIT	3,(IY+dis)	20	15	5
FDCBXX67	BIT	4,(IY+dis)	20	15	5
FDCBXX6E	BIT	5,(IY+dis)	20	15	5
FDCBXX76	BIT	6,(IY+dis)	20	15	5
FDCBXX7E	BIT	7,(IY+dis)	20	15	5
FDCBXX86	RES	0,(IY+dis)	23	19	5
FDCBXX8E	RES	1,(IY+dis)	23	19	5
FDCBXX96	RES	2,(IY+dis)	23	19	5
FDCBXX9E	RES	3,(IY+dis)	23	19	5
FDCBXXA6	RES	4,(IY+dis)	23	19	5
FDCBXXAE	RES	5,(IY+dis)	23	19	5
FDCBXXB6	RES	6,(IY+dis)	23	19	5
FDCBXXBE	RES	7,(IY+dis)	23	19	5
FDCBXXC6	SET	0,(IY+dis)	23	19	5
FDCBXXCE	SET	1,(IY+dis)	23	19	5
FDCBXXD6	SET	2,(IY+dis)	23	19	5
FDCBXXDE	SET	3,(IY+dis)	23	19	5
FDCBXXE6	SET	4,(IY+dis)	23	19	5
FDCBXXEE	SET	5,(IY+dis)	23	19	5
FDCBXXF6	SET	6,(IY+dis)	23	19	5
FDCBXXFE	SET	7,(IY+dis)	23	19	5
FDE1	POP	IY	14	12	5(4)
FDE3	EX	(SP),IY	23	19	8(6)
FDE5	PUSH	IY	15	14	5(4)
FDE9	JP	(IY)	8	6	4
FDF9	LD	SP,IY	10	7	2
FEEXX	CP	NN	7	6	2
FF	RST	38H	11	11	6(5)

Shift and Rotation instructions

